

Electronics Status

- Modification of adaptor card [→Dmitri will talk about this]
- Preparing for 100% test
 - we received 18 JC's (including power and clock cables) and 5+1 AC's
 - Lyn found 9 more 80 conductor cables
 - we have enough 50 conductor cables
 - we have IB, VRB, and VTM, but spare
 - one problematic sequencer
 - need remapping of HV channels and my need debugging bad pods
 - after fixing the AC modifications, we will start to assemble the setup

Testing at electrical support structure

- The read out system is back up
 - now we have BiRa HV system - bias scheme is the same as the real system (we used Keithley before)
 - can be used for testing the modified adaptor card, firmware, etc.
 - can be used for noise study
- To do (for noise study)
 - remove VCAL 0 ohm resistors (as a part of AC modifications)
 - optimize resistor across the isolated and common ground
 - probably more, but what is it?

Resistor across isolated and non-isolated GND



